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Sun and a large and growing number of companies in the computer industry have embarked on a new approach to meet the needs of computer users and system developers in the 1990s.

Originated by Sun, the approach targets users who need a range of compatible computer systems with a variety of application software and want the option to buy those systems from a choice of vendors.

The SPARC Technical Papers | Ben J. Catanzaro | Springer

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Abstract CYC601 processor (integer unit), running at a clock rate of 25–33 MHz, implements the complete set of SPARC instructions in a 0.8 micron CMOS technology.

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Important note: this paper was originally published before the acquisition of Sun

PERFORMANCE CONSIDERATIONS FOR DEVELOPERS UTILIZINGSUN ...

V9 (64-Bit SPARC) Architecture Book (pdf) [399 pages] V9 Errata (html) V8 and V9 Imp Numbers (html) V9 implementation Document (referenced in the V9 Architecture book for 64bit

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SPARC (Scalable Processor Architecture)
is a reduced instruction set computing
(RISC) instruction set architecture (ISA)
originally developed by Sun
Microsystems. Its design was strongly
influenced by the experimental Berkeley
RISC system developed in the early
1980s. First developed in 1986 and
released in 1987, SPARC was one of the
most successful early commercial RISC
systems, and its ...

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SPARC - Wikipedia

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Bechtolsheim, et al, "How Sun Microsystems created SPARCStation 1 using LSI Logic's ASIC system technology," in The SPARC Technical Papers, New York: Springer-Verlag, 1991. Google Scholar 6.

System design methodology of ultraSPARC-I | Proceedings of ...

The SPARC64 V+, code-named "Olympus-B", is a further development of the

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SPARC64 V. Improvements over the SPARC64 V included higher clock frequencies of 1.82–2.16 GHz and a larger 3 or 4 MB L2 cache.. The first SPARC64 V+, a 1.89 GHz version, was shipped in September 2004 in the Fujitsu PrimePower 650 and 850. In December 2004, a 1.82 GHz version was shipped in the PrimePower 2500.

SPARC64 V - Wikipedia

SPARC is a computer architecture derived from the reduced instruction set computer (RISC) lineage. As an architecture, SPARC is not a particular chip or implementation. Rather, SPARC allows for a spectrum of possible price/performance implementations, ranging from microcomputers to supercomputers.

The Scalable Processor Architecture (SPARC) | SpringerLink

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(reduced instructions set computer) high-speed processor, desktop workstations exceeded the capabilities of microcomputers. SPARC, which stands for Scalable ...

The SPARC Technical Papers (eBook, 1991) [WorldCat.org]

SPARCstation 5 Service Manual Part No.:
801-6396-11 Revision A, August 1994
Sun Microsystems Computer Company A
Sun Microsystems, Inc. Business 901 San
Antonio Road Palo Alto, CA 94303-4900
USA

SPARCstation 5 Service Manual - Oracle

SPARC M8 and SPARC M7 Series Servers
Documentation | Provides
documentation for the SPARC M8 and
SPARC M7 servers. Oracle ZFS Storage
ZS3-ES Documentation | Provides
documentation for the Oracle ZFS
Storage ZS3-ES Appliance. Sun
Datacenter InfiniBand Switch 36
Documentation | Provides

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documentation for the Sun Datacenter InfiniBand Switch 36.

Oracle SuperCluster M8 and SuperCluster M7 Documentation

Covers the Sun Microsystems SPARC (Scalable Processor Architecture) high-speed processor technology and the performance levels it elicits from the Sun RISC (reduced instruction set computer). In particular, it focuses on the way SPARC-driven processors run identical software on different machines.

The SPARC technical papers (Book, 1991) [WorldCat.org]

Oracle's New SPARC Systems Deliver 2-7x Better Performance, Security Capabilities, and Efficiency than Intel-based Systems (September 21, 2017)
Fujitsu SPARC M12-1: Impressive Debut (August 27, 2017)
Oracle Announces Breakthrough Processor and Systems Design with SPARC M7 (January 23, 2016)

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Technical Reference Library **White Papers | SPARC International, Inc.**

This paper will provide technical information on how Oracle Solaris and Fujitsu servers designed with the SPARC processor have been highly optimized for each other, improving throughput, security, and resiliency throughout the application solution stack, driving maximum ROI and minimum TCO. It includes brief technical descriptions of how specific

Whitepaper Solaris and SPARC Enterprise

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Open Education - SPARC

3 | A TECHNICAL OVERVIEW OF ORACLE SUPERCLUSTER For example, it provides more processing power with up to 32 SPARC M6 processors from Oracle, more memory with up to 1 TB per physical SPARC M6 processor, more built-in I/O with 64 PCIe Gen 3 slots, and, finally, three levels of virtualization: physical domains (or PDOMs), logical domains (LDOMs),

A Technical Overview of Oracle SuperCluster - white paper

Solution Type Technical Instruction Sure Solution 1007058.1: Sun SPARC Enterprise(R) T5120/5220 Server, ILOM WEB GUI tour [Video]

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